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09/835133  
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ISSUE CLASSIFICATION	
Class	Subclass

**PATENT NUMBER.**

**U.S. UTILITY Patent Application**

<p>O.I.P.E.</p> <p>SCANNED  Q.A. <u>Am</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/835138	CONT/PRIOR F	CLASS 710	SUBCLASS 305	ART UNIT 2181	EXAMINER PHAN, R
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## APPLICANTS

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2111

**TITLE**

Data-processing unit with a circuit arrangement for connecting a first communications bus with a second communications bus

PTO-2040  
12/98[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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